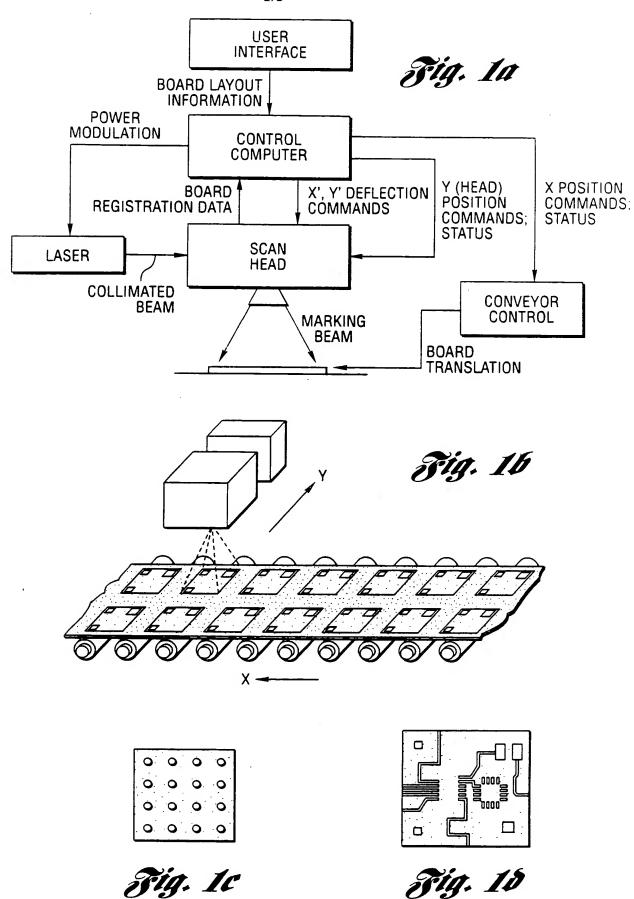
Title. Laser Scanning Method And System For Marking Articles Such As Printed Circuit boards,

Integrated Circuits And The Like First Named Inventor: Joseph P. Downes, et al.

Application Serial No.:

/ Atty. Docket No.: GSIL 0117 PUSA 1

1/3



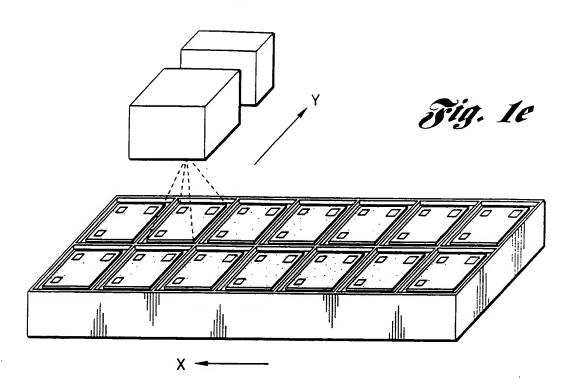
Integrated Circuits And The Like

First Named Inventor: Joseph P. Downes, et al.

Application Serial No.:

/ Atty. Docket No.: GSIL 0117 PUSA 1

2/3



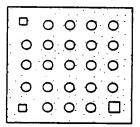
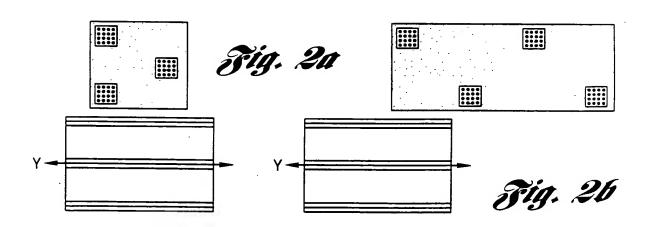


Fig. 1f



Title: Laser Scanning Method And System For Marking Articles Such As Printed Circuit Boards,
Integrated Circuits And The Like

First Named Inventor: Joseph P. Downes, et al.

Application Serial No.:

/ Atty. Docket No.: GSIL 0117 PUSA 1

3/3

